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Department of Computer Science and Engineering. IIT Bombay
Engineering Economic Analysis by Donald G. Newnan, Jerome P. Lavelle, Ted G. Eschenbach

Basic Engineering Circuit Ysis 10th

If there are at most D nested products in an arithmetic expression, we say that it has a product depth of D . Here we study the blow up of size that is involved in converting an algebraic circuits of product depth $D+1$ into an algebraic circuit of depth D when every computation is restricted to multilinear polynomial, and show that the ...

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