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TUTORIAL CADENCE DESIGN ENVIRONMENT

CADENCE MANUAL. Introduction to this manual This manual is intended primarily for students in ESE570: DIGITAL INTEGRATED CIRCUITS AND VLSI FUNDAMENTALS offered at University of Pennsylvania. Basic knowledge of how CMOS transistors operate is required.

Virtuoso Spectre Circuit Simulator RF Analysis User Guide

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Cadence AMS Simulator User Guide Preface September 2000 12 Product Version 1.0 Instance-Based View Switching Application Note Cadence Library Manager User Guide Signalscan Waves User Guide Virtuoso Schematic Composer User Guide Verilog-AMS Language Reference Manual. Available from Open Verilog International. Verilog-XL Reference

Spectre Circuit Simulator Reference

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Spectre X Simulator - Cadence Design Systems

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Cadence User Guide - modularscale.com

environment, under the Cadence® design framework II. To see how the Spectre circuit simulator is run under the analog circuit design environment, read the Cadence Analog Design Environment User Guide. For more information about using the Spectre circuit simulator with SpectreHDL, see the SpectreHDL Reference manual.

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Spectre Circuit Simulator User Guide

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