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## **Chapter 2 The Microprocessor And**

CHAPTER 2 The Microprocessor and its Architecture INTRODUCTION This chapter presents the microprocessor as a programmable device by first looking at its in-ternal programming model and then at how it addresses its memory space.

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Lesson 2: Fetch, Decode, and Execute. Whether you're playing a game, writing a report, or surfing the Web, the microprocessor in a computer processes your data using the same three steps over and over again. It does these three steps at incredible speed—millions of times a second.

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- This chapter presents the microprocessor as a programmable device by first looking at its internal programming model and then how its memory space is addressed.
- The architecture of Intel microprocessors is presented, as are the ways that the

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family members address the memory system.

## **Chapter 2**

The Arithmetic Logic Unit, or ALU is the part of the microprocessor that performs arithmetic operations. ALUs can typically add, subtract, divide, multiply, and perform logical operations of two numbers (and, or, nor, not, etc). ALU will be discussed in far more detail in a later chapter, ALU. Registers

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Clock Rate Limitation in Pipelining o  
Increasing the clock speed  
doesnotguarantee significant  
performance gains. o This is because  
the speed of the processor is  
effectively determined bythe rate at  
which it can fetch instructions and data  
from memory. n Example: if the  
processor spends 90% of its time  
waiting on memory, the performance  
gained by doubling the

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2. 2 Microprocessor Architecture • The microprocessor can be programmed to perform functions on given data by writing specific instructions into its memory. – The microprocessor reads one instruction at a time, matches it with its instruction set, and performs the data manipulation specified.

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Microprocessor can't directly understand programming languages, so programs have to be converted into \_\_\_\_\_ that corresponds to the

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microprocessor's instruction set. Machine language Modern refrigerators, washing machines, and other appliances are controlled by integrated circuits called \_\_\_\_\_ that combine sensors with processing circuitry.

### **Chapter 2: The Microprocessor and its Architecture**

2.2.2. Default Segment and Offset Registers The microprocessor has a set of rules that apply to segments whenever memory is addressed, These rules, which apply in the real and protected mode, define the segment register and offset register combination.

### **Fundamentals of Microprocessor and Chapter 1 Microcontroller**

This microprocessor was superior to



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the 8088 microprocessor in speed because the number of transistors on the microprocessor was increased. Intel also released the 80386 series and the 80486 series of microprocessors that processed data faster than the previous microprocessors.

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A Microprocessor is a multipurpose, Programmable clock-driven, register based electronic device that read binary instruction from a storage device called memory, accepts binary data as input and processes data according to those instructions and provides results as outputs.

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Chapter 1 - Introduction to Microprocessors that Z80 was a very powerful microprocessor in its time. It could address directly 64 Kb of memory, it had 176 instructions, a large number of registers, a built in option for refreshing the dynamic RAM memory, single-supply, greater speed of work etc. Z80 was a great success and

## **Chapter-2**

Chapter 2 The Microprocessor and its Architecture Assoc. Prof. Dr. Gazi Erkan BOSTANCI Slides are mainly based on The Intel Microprocessors by Barry B. Brey, 2008 1 Subscribe to view the full document.

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-a microprocessor that is the main

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processor of a computer-responsible for organizing and carrying out instructions to produce a desired output

### **The Journey Inside?: Curriculum for Microprocessor**

Microprocessor-based Systems -BUS  
n The three components –MPU, memory, and I/O –are connected by a group of wires called the BUS n  
Address bus n consists of 16, 20, 24, or 32 parallel signal lines (wires)  
-unidirectional n these lines contain the address of the memory location to read or written n  
Control bus " consists of 4 to 10 (or more) parallel signal lines

### **Chapter 2-8085 Microprocessor Architecture and ...**

A chip (such as the CPU or GPU) that

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performs processing functions. The chip located on the motherboard of a computer that performs most of the processing for a computer. A CPU that contains the processing components or core of more than one processor in a single CPU. A CPU that contains two separate processing cores.

### **Microprocessor: Chapter 2: Intel 8085 Microprocessor ...**

Clocksperd, also referred to as \_\_\_\_\_, is the speed at which the processor performs the operations required to run a digital device and instructions executed by the digital device.

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