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SPICE Models | Design Center |
Analog Devices
I-V Characteristics of PMOS

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Transistor : In order to obtain the relationship between the drain to source current (I_{DS}) and its terminal voltages we divide characteristics in two regions of operation i.e. linear region and saturation region.. In linear region the I_{DS} will increase linearly with increase in drain to source voltage (V_{DS}) whereas in saturation region the I_{DS} is constant and it ...

Design Of Cmos Operational Amplifiers

18 V, 725 μ A, 4 MHz CMOS RRIO
Operational Amplifier: ADA4666
SPICE Macro Model. ADA4666
SPICE Macro Model; ADA4691-2:
Low Power, 3.6 MHz, Low Noise,
Rail-to-Rail Output, Operational
Amplifiers: ADA4691 SPICE

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Macro Model: ADA4691 SPICE
Macro Model; ADA4692-2: Low
Power, 3.6 MHz, Low Noise, Rail-
to-Rail Output, Operational
Amplifiers: ADA4692 ...

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