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r to reduce the system inherent delay T of the system $P(s)$. C Filtering the input to P , to reduce the effects of the high frequency disturbance w . D Obtaining a more exact approximation \hat{T} of the time delay T .

Explanation: • When comparing the signals \hat{y} and y , it can be seen that the dynamics of the systems are significantly different.

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Exam - Solutions

Sample Examination Digital Control Systems
Page 3 Solution 1 a) The diagram is shown in Figure 2. ? $r(t) = 0$ AAF ADC ?P DAC Plant
 $y(t)$ $d(t)$ $n(t)$ Figure 2: Discrete-time control system b) It is generally advisable to have the crossover frequency of the loop

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