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About Author. Charles Roth is Professor Emeritus in Electrical and Computer Engineering at the University of Texas at Austin, where he taught Digital Design for more than four decades. He is the author of Fundamentals of Logic Design, which is in its sixth edition, and Digital Systems Design using VHDL, which is in its second edition.

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1.5. A static 1-hazard occurs when $A = 0$, $D = 0$, and C changes. When C changes from 1 to 0; $A'C$ also goes from 1 to 0. The hazard occurs because C' hasn't become 1 yet since it has to go ...

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2.21. 2.22. Unlike Cl_r , the output from the mux is only read on falling clock edges; therefore, adding C to the sensitivity list is not required for proper operation of the circuit. 2.23 (a) sel ...

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