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Objectives of this chapter 6 Fundamentals of Power Electronics Chapter 2: Principles of steady-state converter analysis. Develop techniques for easily determining output voltage of an arbitrary converter circuit. Derive the principles of inductor volt-second balance and capacitor charge (amp-second) balance.

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Fundamentals of Power Electronics, Second Edition, is an up-to-date and authoritative text and reference book on power electronics. This new edition retains the original objective and philosophy of focusing on the fundamental principles, models, and technical requirements needed for designing practical power electronic systems while adding a wealth of new material.

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Revision to Fundamentals of Power Electronics Fundamentals of Power Electronics First Edition R. W. Erickson Power Electronics Group, University of Colorado at Boulder About the second edition A new textbook on power electronics converters. This book is intended for use in introductory power electronics courses at the

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Fundamentals of Power Electronics Chapter 1: Introduction 10. Power loss in an ideal switch. Switch closed: v(t) = 0 Switch open: i(t) = 0 In either event: p(t) = v(t) i(t) = 0 Ideal switch consumes zero power. + v(t) = 0

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Course introduction for EE404 Fundamentals of Power Electronics at UNIST.

#### Fundamentals of Power Electronics Instructor's slides

A project-ori ented power electronics design laboratory is offered in parallel with this course serves as a prerequisite for two follow-up courses. The second course starts with Section 7.4, proceeds to Appendi ces B and C, Chapters 10, 11 and 12, and concludes with the material of Chapters 16 to 18.

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