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Dise ñ o con amplificadores operacionales y circuitos integrados....

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Photonic Device Science Laboratory - NAIST

Dise ñ o con amplificadores operacionales y circuitos integrados anal ó gicos, 3ra Edici ó n - Sergio Franco

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In addition, BICMOS can reduce the design time by allowing direct use of many existing cells in realizing a given analog circuit function. On the other hand, the main advantage of pure CMOS is that it offers the lowest overall cost. Twenty years ago, CMOS technologies were only fast enough to support applications at audio frequencies.

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Memristor - Wikipedia

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