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## **Op-Amps and Linear Integrated Circuits by Ramakant A. Gayakwad**

A typical summing amplifier with three input voltages  $V_1$ ,  $V_2$  and  $V_3$  three input resistors  $R_1$ ,  $R_2$ ,  $R_3$  and a feedback resistor  $R_f$ ;  
The following analysis is carried out assuming that the op-amp is an ideal one,  $AOL = \infty$ . Since the input bias current is assumed to be zero, there is no voltage drop across the resistor  $R_{comp}$  and hence the non-inverting input terminal is at ground potential.

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Ramakant A. Gayakwad is the author of Op-Amps and Linear Integrated Circuits (4.04 avg rating, 638 ratings, 41 reviews, published 1983), Lab Manual to Ac...

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### **Op-amps and linear integrated circuits: Ramakant A ...**

Next Page A circuit is said to be linear, if there exists a linear relationship between its input and the output. Similarly, a circuit is said to be non-linear, if there exists a non-linear relationship between its input and output. Op-amps can be used in both linear and non-linear applications.

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