

Rapidio The Embedded System Interconnect

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Serial RapidIO (sRIO) architectures in embedded systems ...

A smarter alternative is RapidIO, an open, standards-based interconnection technology for chip- and board-level communications in medium- and large-size embedded systems. This technology enables high-speed, packet-switched, peer-to-peer connectivity between ASICs, DSPs, FPGAs, microprocessors, network processors and backplanes.

RapidIO: The Embedded System Interconnect / Edition 1 by ...

RapidIO was designed to serve as a low-latency processor interconnect for use in embedded systems requiring high reliability, low latency, and deterministic operation. It was designed to connect different types of processors from different manufacturers in a single system.

RapidIO: The Interconnect Architecture for High ...

RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major reference work for the evaluation and understanding of RapidIO. Covering essential aspects of the specification, it also answers most usage questions from both hardware and software engineers.

RapidIO® Interconnect for Servers and Supercomputing ...

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Sub-microsecond interconnects: PCIe, RapidIO and other ...

This desire for standard embedded system interconnects is the primary impetus behind the development of the RapidIO interconnect technology. 1.6 RAPIDIO: A NEW APPROACH. The RapidIO interconnect architecture is an open standard which addresses the needs of a wide variety of embedded infrastructure applications.

RapidIO: The Interconnect Architecture for High ...

RapidIO is the only next generation intra-system interconnect that uses source routing. PCI full address decode, on the other hand, forces a bridge device to do a match over an entire address width. Technologies such as Infiniband have a major impact on legacy runtime software and are consequently disadvantaged in the embedded market.

Implementing RapidIO - Embedded Processing

RapidIO has its roots in RACEway (ANSI/VITA 5.1), a crossbar interconnect using ring, mesh, or tree topologies. RapidIO technologies represent a leap forward in bandwidth and service diversity for industrial and military users with higher demands on embedded hardware interconnects than can be handled by PCI or Ethernet.

RapidIO: The Embedded System Interconnect: Fuller, Sam ...

Interconnect Landscape RapidIO is categorized as an intra-system interconnect as shown in Figure 3. Specifically, RapidIO is targeted at the intra-system interconnect applications in the high performance embedded equipment market. This market has distinctly different requirements from the desktop and server computer spaces.

RapidIO on tour - Military Embedded Systems

cluster based on RapidIO interconnect, focusing on the band-width capabilities of the technology as well as the scalability. I.

INTRODUCTION RapidIO is a high-performance, low pin count, packet switched system level interconnect standard. RapidIO promises the combined strengths of PCI Express and Ethernet

Simulative Analysis of the RapidIO Embedded Interconnect ...

RapidIO for Multi Processor Systems RapidIO is the interconnect that was developed for peer-to-peer inter-processor communication between devices on a board, between boards across a backplane and between chassis through cabling. RapidIO is optimized for low latency, low power interconnect in embedded systems where

RapidIO - Wikipedia

Abstract—RapidIO is an emerging standard for switched interconnection of processors and boards in embedded systems. In this paper, we use discrete-event simulation to evaluate and prototype RapidIO-based systems with respect to their performance in an environment targeted

towards space-based radar applications.

? Embedded Systems - RapidIO

High-speed, reliable connectivity for next-generation system design. Providing a detailed description of RapidIO applications in several types of system, RapidIO: The Embedded System Interconnect is the first comprehensive reference on the RapidIO interconnect technology. RapidIO was developed specifically to achieve high-performance, low-cost, reliable and scalable system connectivity in ...

RapidIO: The Embedded System Interconnect | Semiconductors ...

– Parallel 8-bit RapidIO @ 500 MHz applied clock • Initial application in embedded systems – Compute, defense, networking & telecom line cards – CPU I/O, Line-card aggregation, backplane – Serial PHY allowed expansion to data plane • Flow control, encapsulation, streams • Positioning – Best solution for embedded systems

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The RapidIO architecture is a high-performance packet-switched interconnect technology. RapidIO supports messaging, read/write and cache coherency semantics. RapidIO fabrics guarantee in-order packet delivery, enabling power- and area- efficient protocol implementation in hardware. Based on industry-standard electrical specifications such as those for Ethernet, RapidIO can be used as a chip-to ...

Rapidio The Embedded System Interconnect

Often, the performance of the transfers across the system interconnect limits the overall system performance. One solution to this interconnect problem is RapidIO (RIO, www.rapidio.org). RapidIO is an industry-standard high-speed switched-packet interconnect that is becoming more common in embedded systems to support the demands of applications which require this model of interconnect and ...

Simulative Analysis of the RapidIO Embedded Interconnect ...

RapidIO was designed to serve as processor fabrics interconnect. RapidIO is most-often used in embedded systems that require high reliability, low latency (typically sub microsecond) and deterministic operation. It is designed to support connecting different types of processors from different manufacturers together in a single system. Because of this it has found widespread use in wireless ...

RapidIO: Optimized for low-latency processor connectivity

This paper describes RapidIO, a high performance, low pin count, packet switched system level interconnect architecture. The interconnect architecture is an open standard which addresses the needs of a wide variety of embedded infrastructure applications. Applications include interconnecting microprocessors, memory, and memory mapped I/O devices in networking equipment, storage subsystems, and ...

Exploring RapidIO Technology within a System Event ...

RapidIO is a high-performance, packet-switched interconnect technology targeted towards processor-to-processor and board-to-board

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interconnection in embedded systems. In this paper, we use simulation to evaluate several systems built around RapidIO interconnects. Specifically, we consider

Rapidio The Embedded System Interconnect

RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major reference work for the evaluation and understanding of RapidIO. Covering essential aspects of the specification, it also answers most usage questions from both hardware and software engineers. It will also serve as a companion text to the specifications when ...

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