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AXI Interface Based ML605/SP605 MicroBlaze Processor Subsystem

SP605 board configuration. Xilinx UG526 document also known as SP605 Hardware User Guide is your best friend if you want to know more details about usage and configuration of this nice board. To load bitstream from onboard SPI flash chip you need to configure SP605 by turning SW1 switches into the 1-ON, 2-OFF position.

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