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Caltech DSP Group: Professor P. P. Vaidyanathan
Palghat P. Vaidyanathan is the Kiyo and Eiko Tomiyasu Professor of Electrical Engineering at the California Institute of Technology, Pasadena, California, USA, where he teaches and leads research in the area of signal processing, especially digital signal processing, and its applications. He has authored four books, and authored or coauthored close to five hundred papers in various IEEE journals and conferences. Prof. Vaidyanathan received his B.Tech and M. Tech degrees from the ...

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(a) Using Appendix 1, find the magnitude spectrum. (b) Find the phase spectrum. Solution (a) From Table A2 in Appendix 1 1 j?f. Xa (f) = Thus the magnitude spectrum is

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He showed how such structures can be designed directly in discrete time, without the need for transforming electrical circuits into the digital domain. An early proponent of applying signal processing methods to genomics, Vaidyanathan developed methods to computationally predict the location of protein coding genes and noncoding genes.

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A discrete-time signal x(n) is defined as (a) Determine its values and sketch the signal x (n). (b) Sketch the signals that result if we: 1. First fold x(n) and then delay the resulting signal by four samples. 2. First delay x(n) by four samples and then fold the resulting signal. (c) Sketch the signal x (—n + 4).

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