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VCS ® MX/VCS MXi User Guide

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G-2012.09 September 2012
Comments? E-mail your
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to:
vcs_support@synopsys.com.

3. Synopsys VCS and VCS MX
Support

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Environment Setup Version

```
2017.03 SP2-2 >source /tools  
/synopsys/vcsmx/m201703sp22/  
cshrc.vcsmx Version 2016.06  
sp2-1 >source /tools/synopsy  
s/vcsmx/l201606sp21/cshrc
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Intended Audience¶. The
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Environment (ASE) User Guide addresses both beginning and experienced developers. To be successful, you should have knowledge and experience in the following areas: C/C++; Verilog/SystemVerilog; RTL

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simulators such as Synopsys VCS-MX* or Mentor Graphics ModelSim-SE* or QuestaSim*

VCS®/VCSi™ User Guide
Vivado Design Suite User
Guide Release Notes,
Installation, and Licensing

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UG973 (v2015.2) June 24, 2015. ... Synopsys VCS and VCS MX (I-2014.03-SP1) Yes Yes Yes N/A N/A N/A The MathWorks MATLAB® and Simulink® with Fixed-Point Toolbox (2014a, 2014b, and 2015a)

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VCS MX (Verilog & VHDL mixed simulator)

The following manuals contain related information about VCS MX:

- VCS MX Reference Guide A manual that provides reference

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information for VCS MX, and is intended to be used in conjunction with the VCS MX User Guide. • VCS MX Quick Reference A small booklet that helps you locate reference information quickly and easily.

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EE 382N: VCS Manual

For assistance, please email
help@ece.cmu.edu Emergency
number: (412) 268-1732

Error-[MPD] Module
previously declared

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which includes the C Series, Profile Series, EX Series and MX G1 Series.

Collaboration Endpoint Software inherits fundamentals from TC software but should be considered as new software

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and not as continuation of TC software. Upgrading from TC7.3.3 and later is supported if your endpoint is already running one of these versions.

vcsmx_ug | Graphical User

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VCS MX/VCS MXi User Guide
Synopsys VCS and VCS MX

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Support ... The VCS User Guide installed with the VCS software, and the Synopsys VCS Simulation Design Example page. Quick Start Example (VCS Verilog) You can adapt the following RTL simulation example to get

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started quickly with VCS: 1. Specify your EDA simulator and executable path in the Quartus II software:

Simulating Verilog RTL using Synopsys VCS

Simulating Verilog RTL using

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Synopsys VCS 6.375 Tutorial
1 February 16, 2006 ... vcs-
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Synopsys waveform viewer
Getting started Before using
the 6.375 tool ow you must

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add the course locker and run the course setup script with

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This user guide includes another tutorial for the VCS

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tools. This tutorial will explain additional features of the debugging interface that you may find useful for this class. For debugging, the VCS 2012 package has the DVE tool (replaced old Virsim). Please invoke DVE

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as follows: Interactive Mode
compile time: >vcs <other
options> -R -gui

Simulating Verilog RTL using
Synopsys VCS
vcs <options that do not
specify source files> <top

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module name>VCS
automatically searches all the libraries specified in the synopsys_sim.setup file for unresolved modules. If you would like more control over the library search order, specify the libraries

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with the `-lib` option documented in the VCS MX User Guide.

Cisco TelePresence MX Series
IP User Guide Documentation
Synopsys VCS/VCS MX
simulator install path/bin

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(Linux). In lab we will be using the simulator only and will not use the synthesis tools. VCS has its About the use model (2), Please refer chapter (3) in VCS User Guide. Kenmore heavy duty 70 series

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Simulating Verilog RTL using Synopsys VCS CS250 Tutorial 4 (Version 091209a)

September 12, 2010 Yunsup

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Lee In this tutorial you will gain experience using Synopsys VCS to compile cycle-accurate executable simulators from Verilog RTL. You will also learn how to use the Synopsys Waveform viewer to trace the various

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signals in your design.

Synopsys | User Guide

The Synopsys VCS® functional verification solution is the primary verification solution used by a majority of the world's top 20

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Functional Unit (AFU)
Simulation ...
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